

ABSTRACT

A write strategy circuit and a write strategy method for capturing data are disclosed. A strategy 5 clock generator generates a strategy clock signal based on a channel clock signal. A phase controller produces a capturing channel clock signal by controlling a phase of the channel clock signal in synchronization with the strategy clock signal. A data capturing circuit captures 10 the data in synchronization with the capturing channel clock signal. A phase determination circuit determines whether a length of the data corresponds to a predetermined value. A strategy correction circuit applies a predetermined strategy correction to the data 15 based on the strategy clock signal. In this case, the phase controller controls the phase of the channel clock signal based on a determination result of the phase determination circuit. An optical disk apparatus using the write strategy circuit or the write strategy method 20 is also disclosed.